

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L6	0	phase (lead or lag) and feedback and (switching or (class D)) amplifier and comparator and PMOS and NMOS	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/11/08 13:44
L7	38	phase (lead or lag) and feedback and amplifier and comparator and PMOS and NMOS	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/11/08 14:36
L8	0	phase (lead or lag) and feedback and amplifier and comparator and PMOSFET and NMOSFET	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/11/08 14:36
L9	11	phase (lead or lag) and feedback and amplifier and comparator and complementary MOSFET	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/11/08 14:36

*INTERFERENCE*  
**EAST Search History**

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	5	PMOS and NMOS and phase (lead or lag) and error amplifier and comparator	US-PGPUB	ADJ	ON	2006/11/08 16:27
L2	1	(PMOS and NMOS and phase (lead or lag) and error amplifier and comparator).clm.	US-PGPUB	ADJ	ON	2006/11/08 16:27
L3	0	(switching amplifier and phase (lead or lag) and error amplifier and comparator).clm.	US-PGPUB	ADJ	ON	2006/11/08 16:28
L4	0	(class D amplifier and phase (lead or lag) and error amplifier and comparator).clm.	US-PGPUB	ADJ	ON	2006/11/08 16:28
L5	0	(PMOSFET and NMOSFET and phase (lead or lag) and error amplifier and comparator).clm.	US-PGPUB	ADJ	ON	2006/11/08 16:28
L6	0	(complementary NEAR3 MOSFET and phase (lead or lag) and error amplifier and comparator).clm.	US-PGPUB	ADJ	ON	2006/11/08 16:28